UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 6,922,367 B2

Page 1 of 2

DATED

APPLICATION NO.: 10/617246 : July 26, 2005

INVENTOR(S)

: Christopher K. Morzano and Wen Li

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column, Line	Reads	Should Read
Item (57), Line 3 Column 1, Line 51	"respective enable signal" "conventional DDR memory device,"	a respective enable signal conventional DDR memory devices,
Column 2, Line 31 Column 3, Lines 31 and 32	"diagram a data strobe" "complimentary"	diagram of a data strobe complementary
Column 3, Line 57	"compliment"	complement
Column 3, Lines 65-67	"Returning to FIG. 1, since the logic circuits 46, 48, the ENA and ENB signals that enable the	Returning to FIG. 1, since the ENA and ENB signals that enable the logic circuits 46, 48 are
	logic circuits 46, 48 are generated by a flip-flop."	generated by a flip-flop 60, they are enabled at the same time.
Column 5, Line 62	"through the nor gate"	through the NOR gate
Column 6, Line 9	"and RAMBUS DRAMs	and RAMBUS DRAMs
	(RDRAMS"),"	("RDRAMS"),
Colum 6, Line 18	"memory arrays 120, 12"	memory arrays 120, 122
Column 7, Line 5	"which the"	with the
Column 8, Line 9	"applied to control input"	applied to a control input
Column 8, Line 13 13-14	"being compliments"	being complements
Column 8, Lines MAN	"logic circuit in enabled"	logic circuit is enabled
Column 8, Line 30	"coupled to reset"	coupled to the reset
Column 9, Line 23	"applied to an control input"	applied to a control input
Column 9, Line 27	"compliments"	complements
Column 9, Lines 28-29	"logic circuit in enabled"	logic circuit is enabled
Column 9, Lines 43-44	"coupled to reset input"	coupled to the reset input
Column 10, Line 33	"coupled the external data terminal"	coupled to the external data terminal
Column 10, Line 40	"applied to an control input"	applied to a control input
Column 10, Line 44	"being compliments"	being complements
Column 10, Lines 45-46	"second logic circuit in enable"	second logic circuit is enabled

Note

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Column, Line	Reads	Should Read
Column 10, Line 61	"coupled to reset"	coupled to the reset
Column 12, Line 12	"applied to an control input"	applied to a control input
Column 12, Line 16	"compliments"	complements
Column 12, Lines 17-18	"logic circuit in enabled"	logic circuit is enabled
Column 13, Lines 28-29	"being operable to generate make the first enable signal"	being operable to make the first enable signal
Column 13, Line 43	"applied to an control input"	applied to a control input
Column 13, Line 47	"compliments"	complements
Column 13, Line 48-49	"logic circuit in enabled"	logic circuit is enabled
Column 13, Line 63	"circuit farther"	circuit further
Column 15, Line 28	"applied to an control input"	applied to a control input
Column 15, Line 32	"compliments"	complements
Column 15, Lines 33-34	"logic circuit in enabled"	logic circuit is enabled
Column 16, Lines 5 and 17	"the method comprises:"	the method comprising:

This certificate supersedes certificate of correction 1950ed December 25, 2007.

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